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** CONTINUING DATA *****

This application is a DIV of 10/008,616 10/23/2001 PAT 6,746,897
 which is a CON of 09/487,682 01/19/2000 PAT 6,365,432
 which is a DIV of 09/326,316 06/07/1999 ABN
 which is a CON of 08/716,362 09/18/1996 PAT 5,976,912

** FOREIGN APPLICATIONS *****

JAPAN 6-48760 03/18/1994
 JAPAN 6-273469 11/08/1994
 JAPAN 7-7683 01/20/1995
 JAPAN 7-56202 03/15/1995

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35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	COUNTRY	DRAWING	CLAIMS	CLAIMS
Verified and Acknowledged Examiner's Signature: <i>[Signature]</i> Initials: <i>[Initials]</i>	JAPAN	24	10	2

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TITLE

Fabrication process of semiconductor package and semiconductor package

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